REMARKS

Claims 4, 5, 13, and 14 were identified as containing allowable subject matter.

Claim 1 is amended to include the limitations of claims 2-4, which are now canceled, claim 5 is amended to depend from claim 1 and for consistent terminology with claim 1, claims 7 and 8 are canceled, claim 9 is amended to include limitations similar to those of amended claim 1, claim 10 is amended to include the limitations of claims 11-13, which are now canceled, and claim 14 is amended to depend from claim 10 and for consistent terminology.

The amendments are made for the purpose of expediting prosecution and not for patentability, and the claim cancellations are made without prejudice. Applicants reserve the right to pursue subject matter of the original claims (prior to amendment) and subject matter of the canceled claims in subsequent prosecution.

New claim 15 is added to claim the invention in alternative language. The claim is thought to be patentable over the cited prior art because the combination of limitations does not appear to be suggested by the prior art. Claim 15 sets forth a payment terminal. The payment terminal includes a keypad, a card reader, a display, a processor, a first RAM, a second RAM, a register, a power supply, and a security circuit. The processor is coupled to the keypad, card reader and display, and is configured to process transaction payment information input via the keypad and card reader. The first RAM and the register are internal to the processor, and the second RAM is external to and coupled to the processor. The power supply is coupled to the processor, first and second RAMs, the register, and the security circuit. The power supply provides power from a first power source when power is available from the first source and from a second power source when power is unavailable from the first source. The security circuit is coupled to the processor and to the power supply and is configured to detect a security threat to the power supply and generate a first signal in response to detection of a security threat. The processor is configured to store an encryption key in the first RAM while power is available from the first source, responsive to application of power from the second power source copy the sensitive data from the first RAM to the register and erase the sensitive data from the first RAM, responsive to the first signal erase the encryption key from the first RAM and from the register, and generate encrypted

data using the encryption key and store the encrypted data in the second RAM. Since this combination of limitations is not suggested by the prior art, claim 15, and claims 16 and 17 depending therefrom, are thought to be in condition for allowance.

The rejection of claims 2-5, 10-14 under 35 USC §112, second paragraph, as being indefinite is respectfully traversed. The claims as originally filed are understood to be reasonably clear to those skilled in the art. As to claim 2, recitation of "the RAM" would be reasonably understood to correspond to the "first memory element" since there is no other memory referenced in claim 2. Similarly, claims 10 and 11 are thought to be reasonably clear. The claims have been amended, however, for purposes of expediting prosecution.

The rejection of claims 1-3, 6, and 9-12 as being anticipated by "Rager" (US patent 5,363,447 to Rager et al.) under 35 USC §102(b) is respectfully traversed because all the limitations do not appear to be taught by Rager. However, the claims are amended for purposes of expediting prosecution, and the rejection is now moot.

Withdrawal of the rejections and reconsideration of the claims are respectfully requested in view of the amended claims and remarks set forth above. No extension of time is believed to be necessary for consideration of this response. However, if an extension of time is required, please consider this a petition for a sufficient number of months for consideration of this response. If there are any additional fees in connection with this response, please charge Deposit Account No. 50-0996 (HPCO.081PA).

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